

SP40P11

1. Compliance ISO 11784/11785 HDX (Half duplex) system specification
2. FSK modulation with RF 134.2KHz and 123.2KHz two carry frequency
3. Reader provide energy to Tag in the Charge phase only
4. Measure sensor charge time in three channels for one reference sensor (A0(Rref))
5. and two measurement sensors (A1(Rs) and B1(Rse))
6. Built-in 16 bits CRC-CCITT encode and NRZ data encode
7. Built-in 64 bits OTP ROM to setting ID codes
8. There are 128 bits (or 167 bits) serial data in the Transmit phase
9. There are 2 Metal Options (CHNS and SENS) for transmission format selections
10. There are 4 input Pads (FT0~ FT3) for RF frequency tuning